

HIGH SPEED INTERFACE TYPE SEMICONDUCTOR MEMORY DEVICE

ABSTRACT OF THE DISCLOSURE

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The present invention discloses a high speed interface type semiconductor memory device which can transmit data of a plurality of DRAMs of a module to a controller by using only one data strobe clock signal. The high speed interface type semiconductor memory device includes
10 a DRAM module unit for generating a strobe clock signal for synchronizing a data signal in a read operation in a DRAM farthest from a controller among a plurality of DRAMs, providing the strobe clock signal to the other DRAMs, and transmitting data to the controller in the read operation, and a controller for transmitting a clock signal and data signals synchronized with the clock
15 signal to the plurality of DRAMs, and receiving data signals from the DRAMs.

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